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 ... 3. Direct **memory** access (DMA) I/O has precedence over interrupt I/O ... of active DMA devices), the use of **burst-mode** DMA or, on the Q-bus, **block-mode** DMA could ...

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Port: COM1 The **serial** interface port ... sometimes referred to as an RS-**232** port or ...

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be written before it ... The **serial** transmitters are double buffered, while the ...

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memory and the display elements*. ... (also see: Data) **MEMORY** A ...

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... " ause. Seine wichtigsten Teile sind der Zentralprozessor (central processing unit, CPU), und der Arbeitsspeicher (**memory**). Um recht in Freuden arbeiten zu k ...

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... **Memory**. ... COM2 -**Serial Port** This connector allows mouse or the other RS-232 device which use this type connector to transfer data between computer and devices. ...

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... DiskOnChip 2000 series, **memory** capacity from 2 MB to 72 MB • Two enhanced multi-mode SPP/EPP/ECP parallel ports, four **serial** ports: three RS-232 and one RS ...

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... System **memory**: 8MB to 128MB. Two 72-pin SIMM sockets onboard. ... **Serial** ports: Three RS-232 and one RS-232/422/485 **serial** ports. ...

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... This feature is used for **burst mode** operation ... data transfers in the burst or **block mode** because of ... and the controller always making four accesses to the **memory**. ...

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... up to 2MB of secondary synchronous **burst-mode** cache & Display ... 230 MHz pixel clock ó Shared System **Memory** Area 2MB ... ó Two NS16550 Compatible RS-232C **Serial Ports** ...

www.actac.com.tw/%5Cabout%5Cmanual%5CACF-502_manual.pdf - Supplemental Result - [Similar pages](#)

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

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





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Bintjas, C.; Vlachos, K.; Pleros, N.; Avramopoulos, H.;

Lightwave Technology, Journal of , Volume: 21 , Issue: 11 , Nov. 2003

Pages:2629 - 2637

[\[Abstract\]](#) [\[PDF Full-Text \(462 KB\)\]](#) IEEE JNL

2 Generating series and performance bounds for convolutional codes over burst-error channels

Pimentel, C.;

Vehicular Technology, IEEE Transactions on , Volume: 51 , Issue: 5 , Sept. 2002

Pages:1011 - 1017

[\[Abstract\]](#) [\[PDF Full-Text \(529 KB\)\]](#) IEEE JNL

3 Novel control channel quality improvement in satellite communication systems employing high coding gain FEC

Morikura, M.; Kubota, S.; Enomoto, K.; Kato, S.;

Global Telecommunications Conference, 1988, and Exhibition. 'Communications for the Information Age.' Conference Record, GLOBECOM '88., IEEE , 28 Nov.-1 Dec. 1988

Pages:136 - 140 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) IEEE CNF

4 Error Distribution and Diversity Performance of a Frequency-Differential PSK HF Modem

Porter, G.;

Communications, IEEE Transactions on [legacy, pre - 1988] , Volume: 16 , Issue: 4 , Aug 1968

Pages:567 - 575

[\[Abstract\]](#) [\[PDF Full-Text \(776 KB\)\]](#) IEEE JNL

5 An Efficient Parity Checking Scheme for Random and Burst Errors

Murthy, B.;

Communications, IEEE Transactions on [legacy, pre - 1988] , Volume: 24 , Issue: 2 , Feb 1976

Pages:249 - 254

[\[Abstract\]](#) [\[PDF Full-Text \(536 KB\)\]](#) IEEE JNL

6 Baseband Processing in a High-Speed Burst Modem for a Satellite-Switched TDMA System

Acampora, A.; Langseth, R.;

Communications, IEEE Transactions on [legacy, pre - 1988] , Volume: 27 , Issue: 10 , Oct 1979

Pages:1496 - 1503

[\[Abstract\]](#) [\[PDF Full-Text \(880 KB\)\]](#) IEEE JNL

7 A 20 Mhz BiCMOS peak detect pulse qualifier and area detect servo demodulator for hard disk drive servo loop

Mei-Tjing Huang; Spagna, F.; Blink, J.;

Solid-State Circuits Conference, 1997. Digest of Technical Papers. 44th ISSCC., 1997 IEEE International , 6-8 Feb. 1997

Pages:326 - 327, 480

[\[Abstract\]](#) [\[PDF Full-Text \(980 KB\)\]](#) IEEE CNF

8 Analysis of ignitron switching characteristics for burst mode operation of dual and off resonance high frequency power supplies

McDonald, D.; Phillips, M.; Dollinger, R.; Sarjeant, W.J.;

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